WEST Search History

| Hide Items | Restore | Clear | Cancel |

DATE: Monday, March 05, 2007

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=AND	
☐	L36	L35 and (L34 or L33 or L32 or L31 or L30 or L29 or L28 or L27 or L26 or L25 or L24 or L23 or L22 or L21 or L20 or L19)	5
☐	L35	(6069829 or 6208583 or 6653867 or 5675615 or 6782064).pn.	5
		DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=AND	
☐	L34	("7053675")[URPN]	0
☐	L33	(6782064 | 5675615 | 6653867 | 5357146)![PN]	4
☐	L32	("7053675")[PN]	1
		DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=AND	
☐	L31	7053675.pn.	1
		DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=AND	
☐	L30	("5581512"| "6069829")[URPN]	59
		DB=USPT,PGPB; THES=ASSIGNEE; PLUR=YES; OP=AND	
☐	L29	('5581512'| '6041002'| '6061294'| '6069829')![pn]	4
		DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=AND	
☐	L28	independent$ near5 enabl$ with recei$ near6 clock near2 signal	9
☐	L27	(enabl$ or disabl$) near3 access with predetermin$ near5 value with signal	23
☐	L26	memory near3 access with predetermin$ near5 value with disabl$ near5 signal	0
☐	L25	memory near3 access with predetermin$ near5 value with enabl$ near5 signal	13
☐	L24	interface with receiv$ with clock near3 signal near6 processor	114
☐	L23	interface with reveiv$ with clock near3 signal near6 processor	0
☐	L22	L21 and (read or write) near2 port	7
☐	L21	L20 and processing with response with signal near4 clock	318
☐	L20	processor with clock adj2 signal	9541
		DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=AND	
☐	L19	processor with clock adj2 signal	11830
		DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=AND	
☐	L18	L17 and (L16 or L15 or L14 or L13 or L12 or L11 or L10 or L9 or L8 or L7 or L6 or L5 or L4 or L3 or L2 or L1)	5
☐	L17	(6069829 or 6208583 or 6653867 or 5675615 or 6782064).pn.	5
		DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=AND	
☐	L16	("7053675")[URPN]	0

☐	L15	(6782064 | 5675615 | 6653867 | 5357146)![PN]	4
☐	L14	("7053675")[PN]	1

DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=AND

☐	L13	7053675.pn.	1

DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=AND

☐	L12	("5581512"| "6069829")[URPN]	59

DB=USPT,PGPB; THES=ASSIGNEE; PLUR=YES; OP=AND

☐	L11	('5581512'| '6041002'| '6061294'| '6069829')![pn]	4

DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=AND

☐	L10	independent$ near5 enabl$ with recei$ near6 clock near2 signal	9
☐	L9	(enabl$ or disabl$) near3 access with predetermin$ near5 value with signal	23
☐	L8	memory near3 access with predetermin$ near5 value with disabl$ near5 signal	0
☐	L7	memory near3 access with predetermin$ near5 value with enabl$ near5 signal	13
☐	L6	interface with receiv$ with clock near3 signal near6 processor	114
☐	L5	interface with reveiv$ with clock near3 signal near6 processor	0
☐	L4	L3 and (read or write) near2 port	7
☐	L3	L2 and processing with response with signal near4 clock	318
☐	L2	processor with clock adj2 signal	9541

DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=AND

☐	L1	processor with clock adj2 signal	11830

END OF SEARCH HISTORY

The Contents of Case 10815982

Qnum	Query	DB Name	Thesaurus	Operator	Plura
Q1	processor with clock adj2 signal	PGPB,USPT,EPAB,JPAB,DWPI,TDBD	ASSIGNEE	AND	YES
Q2	processor with clock adj2 signal	PGPB,USPT	ASSIGNEE	AND	YES
Q3	Q2 and processing with response with signal near4 clock	PGPB,USPT	ASSIGNEE	AND	YES
Q4	Q3 and (read or write) near2 port	PGPB,USPT	ASSIGNEE	AND	YES
Q5	interface with reveiv$ with clock near3 signal near6 processor	PGPB,USPT	ASSIGNEE	AND	YES
Q6	interface with receiv$ with clock near3 signal near6 processor	PGPB,USPT	ASSIGNEE	AND	YES
Q7	memory near3 access with predetermin$ near5 value with enabl$ near5 signal	PGPB,USPT	ASSIGNEE	AND	YES
Q8	memory near3 access with predetermin$ near5 value with disabl$ near5 signal	PGPB,USPT	ASSIGNEE	AND	YES
Q9	(enabl$ or disabl$) near3 access with predetermin$ near5 value with signal	PGPB,USPT	ASSIGNEE	AND	YES
Q10	independent$ near5 enabl$ with recei$ near6 clock near2 signal	PGPB,USPT	ASSIGNEE	AND	YES
Q11	('5581512'| '6041002'| '6061294'| '6069829')![pn]	USPT,PGPB	ASSIGNEE	AND	YES
Q12	("5581512"| "6069829") [URPN]	USPT	ASSIGNEE	AND	YES
Q13	7053675.pn.	PGPB,USPT	ASSIGNEE	AND	YES
Q14	("7053675")[PN]	USPT	ASSIGNEE	AND	YES
Q15	(6782064 | 5675615 | 6653867 | 5357146)![PN]	USPT	ASSIGNEE	AND	YES
Q16	("7053675")[URPN]	USPT	ASSIGNEE	AND	YES
Q17	(6069829 or 6208583 or 6653867 or 5675615 or 6782064).pn.	PGPB,USPT	ASSIGNEE	AND	YES
Q18	Q17 and (Q16 or Q15 or Q14 or Q13 or Q12 or Q11 or Q10 or Q9 or Q8 or Q7 or Q6 or Q5 or Q4 or Q3 or Q2 or Q1)	PGPB,USPT	ASSIGNEE	AND	YES

Q19	processor.ti.ab.	PGPB,USPT	ASSIGNEE	AND	YES
Q20	processor.ti,ab.	PGPB,USPT	ASSIGNEE	AND	YES
Q21	Q20 and clock.ti.ab.	PGPB,USPT	ASSIGNEE	AND	YES
Q22	Q20 and clock.ti,ab.	PGPB,USPT	ASSIGNEE	AND	YES
Q23	Q22 and interfac$.ti,ab. and port.ti,ab.	PGPB,USPT	ASSIGNEE	AND	YES
Q24	(("20020137533" | "20050068974")!.PN.) .p148-p170. | (("6192430" | "5752071" | "5953509")!.PN.) .p66-p147.	PGPB,USPT	ASSIGNEE	AND	YES
Q25	access near3 enabl$ with predetermin$ near3 value	PGPB,USPT	ASSIGNEE	AND	YES
Q26	Q25 and (processor and interface).ti,ab.	PGPB,USPT	ASSIGNEE	AND	YES
Q27	(("6473821")!.PN.) .p66-p147.	USPT	ASSIGNEE	AND	YES
Q28	(("6473821")!.PN.) .p66-p147. or Q24	PGPB,USPT	ASSIGNEE	AND	YES
Q29	(("5752071" | "6473821")!.PN.) .p66-p147.	USPT	ASSIGNEE	AND	YES